

Serial No.: 10/583,844

PF030184

Amendments to the Claims**RECEIVED
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Please amend claim 1 as indicated.

1. (Currently Amended) Display screen including:
 - light emitters arranged as rows of light emitters and columns of light emitters to form an array of light emitters,
 - a silicon substrate on which control means to control the emissions of the light emitters are fabricated, the said control means including:
 - means for powering the light emitters,
 - a plurality of addressing electrodes arranged according to the columns of light emitters, and intended to transmit a voltage representing an image datum to each column of light emitters,
 - a plurality of selection electrodes arranged according to the rows of light emitters, and intended to transmit a selection signal to each row of light emitters,
 - a plurality of modulation transistors, each associated with a light emitter of the array, the said modulation transistors including a gate electrode intended to be connected to an addressing electrode and two current-carrying electrodes, each modulation transistor intended to have a drain current pass through it to power the said light emitter for a voltage between its gate electrode and one of its current-carrying electrodes that is greater than or equal to a threshold trigger voltage, the said modulation transistors being arranged in columns associated with the columns of light emitters and being geometrically aligned on the substrate ~~according to~~ along a guiding line,
 - a load capacitor connected to the terminals of each modulation transistor and intended to set an electric potential at the gate electrode of the associated modulation transistor, and

Serial No.: 10/583,844

PF030184

a plurality of compensating transistors intended to compensate for the threshold trigger voltage of the modulation transistors by adjusting the charge on the capacitor,

wherein a single compensating transistor is connected to all the modulation transistors of a given column and is intended to compensate for the threshold trigger voltages of all the said modulation transistors of this column, and

wherein the said compensating transistor of a given column is formed in the geometrical extension of the line-arrangement of the said modulation transistors of said given column ~~according to~~ along the said same guiding line.

2. (Previously Presented) Display screen according to Claim 1, wherein the control means do not include any means allowing the flow of current from any one of the addressing electrodes to the means for powering the light emitters.
3. (Previously Presented) Display screen according to Claim 1, wherein the control means include at least one voltage generator connected to one or to each addressing electrode in order to transmit a voltage representing an image datum.
4. (Previously Presented) Display screen according to Claim 1, wherein the compensating transistor of each column of light emitters includes two current-carrying electrodes, each current-carrying electrode being connected in series between the addressing electrode of this same column and the modulation transistors of this same column.
5. (Previously Presented) Display screen according to Claim 1, wherein each compensating transistor includes a gate electrode and two current-carrying electrodes, the gate electrode of each compensating transistor being connected

Serial No.: 10/583,844

PF030184

to the gate electrode of all the modulation transistors of the associated column, in that one current-carrying electrode of each compensating transistor is connected to the addressing electrode of the associated column of light emitters, and in that the other current-carrying electrode of each compensating transistor is connected to its gate electrode.

6. (Previously Presented) Display screen according to Claim 1, wherein the said modulation transistors and the said associated compensating transistor are fabricated on a polycrystalline silicon substrate obtained by heating an amorphous silicon substrate, using a laser beam, the said beam being intended first to heat a first rectangular heating surface of the substrate, then to move in a direction of movement and then to heat a second rectangular heating surface, and

wherein the said modulation transistors associated with the light emitters of a given column and the associated compensating transistor are aligned in one and the same heating surface, the guiding alignment line extending approximately perpendicularly to the direction of movement of the laser beam.

7. (Previously Presented) Display screen according to Claim 1, wherein the said modulation transistors and the said associated compensating transistor each include a channel between two layers of doped material, the said channel being connected to their gate electrode, and in that the channel of the modulation transistors of a column and the channel of the associated compensating transistor have a main axis approximately parallel to the said guiding line.

8. (Previously Presented) Display screen according to Claim 1, wherein the control means include initialization means for initializing the load capacitors intended to discharge all the load capacitors connected to the modulation transistors of a column.

Serial No.: 10/583,844

PF030184

9. (Previously Presented) Display screen according to Claim 8, wherein the initialization means include an initialization transistor having a gate electrode and two current-carrying electrodes, one current-carrying electrode of the said initialization transistor being connected to the gate electrode of the modulation transistors of the said column, the gate electrode of the said initialization transistor being connected to a current-carrying electrode and to the addressing electrode of a column of light emitters.

10. (Previously Presented) Display screen according to Claim 8, wherein the initialization means include a diode, the cathode of which is connected to the gate electrode of the modulation transistors and the anode of which is connected to the addressing electrode of a column of light emitters.

11. (Previously Presented) Display screen according to claim 1, wherein the control means include a plurality of selection transistors having a gate electrode and two current-carrying electrodes, each selection transistor having one current-carrying electrode connected to a modulation transistor, a gate electrode connected to a selection electrode and one current-carrying electrode connected to the compensating transistor of a column of light emitters.

12. (Previously Presented) Display screen according to Claim 1, wherein the light emitters are organic electroluminescent diodes.

13. (Previously Presented) Method for driving a display screen according to Claim 1, wherein the method includes a step for applying a voltage representing an image datum to each addressing electrode of each column of light emitters.